LISTING OF CLAIMS

A detailed listing of claims is presented below. Please amend currently amended claims

as indicated below including substituting clean versions for pending claims with the same

number. In addition, clean text versions of pending claims not being currently amended that are

under examination are also presented. It is understood that any claim presented in a clean

version below has not been changed relative to the immediate prior version.

1. (Currently amended) An alarm clock IC adapted for use in a personal computer (PC) having

a system Real Time Clock (RTC), comprising alarm clock logic circuitry adapted to receive a

signal indicative of the a power status of said PC, adapted to adjust time inside said alarm

clock IC to match with said system RTC, and further adapted to generate an alarm clock event

at a preselected time.

2. (Currently amended) The An alarm clock IC as claimed in claim 1, wherein said alarm clock

event comprises the generation of a control signal to control an AM/FM radio module

associated with said PC.

3. (Currently amended) The An alarm clock IC as claimed in claim 1, wherein said alarm clock

event comprises the generation of a control signal to control a TV module associated with said

PC.

4. (Currently amended) The An alarm clock IC as claimed in claim 1, wherein said alarm clock

event comprises the generation of a control signal to control an audio circuit associated with

said PC.

O2-0142

Serial No.: 10/692,211 2

Examiner: Weinman, S. M.

Group Art Unit: 2115

5. (Currently amended) The An alarm clock IC as claimed in claim 1, further comprising power

control circuitry and wherein said alarm clock event comprises the generation of a control

signal to control said power control circuitry to turn ON said PC system based upon said

signal indicative of the power status of said PC.

6. (Currently amended) The An alarm clock IC as claimed in claim 1, further comprising power

control circuitry and wherein said alarm clock event comprises the generation of a control

signal to control said power control circuitry to turn OFF said PC system based upon said

signal indicative of the power status of said PC.

7. (Currently amended) The An alarm clock IC as claimed in claim 1, wherein said alarm clock

event comprises the generation of a control signal to launch an application program associated

with said PC, said application program adapted to control one or more modules associated

with said PC.

8. (Currently amended) The An alarm clock IC as claimed in claim 1, further comprising a user

input interface adapted to permit a user to control the functionality of said alarm clock logic

circuitry.

9. (Currently amended) The An alarm clock IC as claimed in claim 1, further comprising a

3

display module interface adapted to control a display and adapted to display status

information related to said alarm clock logic circuitry.

Serial No.: 10/692,211

Group Art Unit: 2115

10. (Currently amended) The An alarm clock IC as claimed in claim 1, further comprising a

host interface adapted to interface said alarm clock logic circuitry to a bus associated with said

PC.

11. (Currently amended) An alarm clock PC system, comprising:

a PC adapted to generate a signal indicative of the a power status of said PC and having a

system Real Time Clock (RTC); and

an alarm clock IC adapted to receive said signal indicative of the power status of said PC,

adapted to adjust time inside said alarm clock IC to match with said system RTC, and further

adapted to generate an alarm clock event at a preselected time.

12. (Currently amended) The An alarm clock PC system as claimed in claim 11, wherein said

alarm clock event comprises the generation of a control signal to control an AM/FM radio

module associated with said PC.

13. (Currently amended) The An alarm clock PC system as claimed in claim 11, wherein said

alarm clock event comprises the generation of a control signal to control a TV module

associated with said PC.

14. (Currently amended) The An alarm clock PC system as claimed in claim 11, wherein said

alarm clock event comprises the generation of a control signal to control an audio circuit

associated with said PC.

O2-0142

Serial No.: 10/692,211

Examiner: Weinman, S. M. 4 Group Art Unit: 2115

15. (Currently amended) The An alarm clock PC system as claimed in claim 11, said alarm

clock IC further comprising power control circuitry and wherein said alarm clock event

comprises the generation of a control signal to control said power control circuitry to turn ON

said PC system based upon said signal indicative of the power status of said PC.

16. (Currently amended) The An alarm clock PC system as claimed in claim 11, said alarm

clock IC further comprising power control circuitry and wherein said alarm clock event

comprises the generation of a control signal to control said power control circuitry to turn

OFF said PC system based upon said signal indicative of the power status of said PC.

17. (Currently amended) The An alarm clock PC system as claimed in claim 11, wherein said

alarm clock event comprises the generation of a control signal to launch an application

program associated with said PC, said application program adapted to control one or more

modules associated with said PC.

18. (Currently amended) The An alarm clock PC system as claimed in claim 11, said alarm

clock IC further comprising a user input interface adapted to permit a user to control the

functionality of said alarm clock logic circuitry.

19. (Currently amended) The An alarm clock PC system as claimed in claim 11, said alarm

clock IC further comprising a display module interface adapted to control a display and

5

adapted to display status information related to said alarm clock logic circuitry.

Examiner: Weinman, S. M.

Serial No.: 10/692,211

Group A

Group Art Unit: 2115

20. (Currently amended) The An alarm clock PC system as claimed in claim 11, said alarm

clock IC further comprising a host interface adapted to interface said alarm clock logic

circuitry to a bus associated with said PC.

21. (Currently amended) A method of operating a PC as an alarm clock, said method

comprising the steps of:

monitoring <u>a the</u> power status of said PC;

matching time of said alarm clock with a system Real Time Clock (RTC); and

generating an alarm clock event at a preselected time.

22. (Currently amended) The A method as claimed in claim 21, further comprising the step of,

in response to said alarm clock event, controlling an AM/FM radio module associated with

said PC.

23. (Currently amended) The A method as claimed in claim 21, further comprising the step of,

in response to said alarm clock event, controlling a TV module associated with said PC.

24. (Currently amended) The A method as claimed in claim 21, further comprising the step of,

in response to said alarm clock event, controlling an audio circuit associated with said PC.

25. (Currently amended) The A method as claimed in claim 21, further comprising the step of,

in response to said alarm clock event, generating a control signal to turn ON said PC system

based upon said power status of said PC.

26. (Currently amended) The A method as claimed in claim 21, further comprising the step of,

in response to said alarm clock event, generating a control signal to turn OFF said PC system

based upon said power status of said PC.

27. (Currently amended) The A method as claimed in claim 21, further comprising the step of,

in response to said alarm clock event, launching an application program associated with said

PC, said application program adapted to control one or more modules associated with said PC.

28. (Currently amended) The A method as claimed in claim 21, further comprising the step of

displaying status information related to said alarm clock event.

Examiner: Weinman, S. M.